Solutions - Quiz 2

(October 7th @ 5:30 pm)

PROBLEM 1 (40 PTS)

- Complete the following table. Use the fewest number of bits in each case:

REPRESENTATION			
Decimal	**Sign-and-magnitude**	**1's complement**	**2's complement**
-2	110	101	10
-8	11000	10111	1000
-4	1100	1011	100
5	0101	0101	0101

- Convert the following decimal number to its 2's complement representation: -13.25 (5 pts)

  $13.25 = 01101.01_2 \rightarrow -13.25 = 10010.11_2$

PROBLEM 2 (20 PTS)

- Perform the following operation in the 2's complement representation, i.e., provide the summands and the result in 2's complement representation. Use the minimum number of bits to represent both the summands and the result so that the overflow bit is 0.

  ✓ -11 + 17

n = 6 bits
$c_6 \oplus c_5 = 0$
No Overflow

	c_6=1	c_5=1	c_4=0	c_3=0	c_2=0	c_1=1	c_0=0	
-11 =	1	1	0	1	0	1	+	
17 =	0	1	0	0	0	1		
6 =	0	0	0	1	1	0		

$-11 + 17 = 6 \in [-2^5, 2^5-1] \rightarrow$ no overflow

PROBLEM 3 (40 PTS)

- Complete the timing diagram of the circuit shown below: